## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of

PATENT APPLICATION

Inventor:

Apostolos Voutsas, Yukihiko Nakata, and Takeshi Hosoda February 20, 2002

Serial No.:

09/696,813

ATTORNEY DOCKET

No. SLA0468

Filed:

October 25, 2000

Group Art Unit: 2826

Title:

SEMICONDUCTOR DEVICE

Examiner:

AND A METHOD OF CREATING THE SAME Minhloan Tran

UTILIZING METAL INDUCED) CRYSTALLIZATION WHILE

SUPPRESSING PARTIAL SOLID PHASE

CRYSTALLIZATION

2800 MAIL ROOM CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Hon. Commissioner for Patents, Washington, D.C. 20231 on February 20, 2002.

C. Ripma, Reg. No. 27,672

Signature Date: February 20, 2002

RESPONSE TO RESTRICTION REQUIREMENT

Hon. Commissioner for Patents Washington, D.C. 20231

Sir:

This communication responds to an Office Action dated



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3	REM	19	
4	XT/	1	

Total number	of	pages:	25
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